## Amendments to the Claims:

## Claims 1-7 (canceled).

Claim 8 (currently amended) A method for processing a surface of a semiconductor wafer comprising the steps of:

- removing a material layer overlying a barrier layer from the surface of the semiconductor wafer at a first chemical-mechanical polishing station with a chemical-mechanical polishing pad and using an abrasive slurry; and
- removing the barrier layer from the surface of the semiconductor water at a buff station using a buff pad and a first barrier layer slurry.

Claim 9 (original) The method of claim 8, further comprising the step of buffing the wafer surface after barrier layer removal.

Claim 10 (previously presented) The method of claim 9, wherein a set of buff station parameters are different for the barrier layer removal step than for the buffing step.

Claim 11 (currently amended) The method of claim 8.2, wherein a different slurry composition is used for the barrier layer removal step than for the buffing step.

Claim 12 (original) The method of claim 8, further comprising the step of detecting when the material layer is substantially removed from the water

Claim 13 (original) The method of claim 8, further comprising the step of detecting a point at which barrier layer removal is substantially complete.

Claim 14 (previously presented) The method of Claim 13, wherein the step of detecting a point at which barrier layer removal is substantially complete is accomplished using an endpoint detection system.

Claim 15 (previously presented) The method of claim 14, wherein the endpoint detection system is comprised of an optical detection system.

Claim 16 (previously presented) The method of Claim 14, wherein the endpoint detection system is comprised of an infra red detection system.

Claim 17 (previously presented) The method of Claim 14, wherein the endpoint detection system is comprised of a laser detection system.

Claim 18 (previously presented) The method of Claim 14, wherein the endpoint detection system is comprised of a motor current detection system.

Claim 19 (original) The method of claim 8 further comprising the step of conditioning the buff station pads.

Claim 20 (original) The method of claim 19 wherein the conditioning step is accomplished by pressing a lower buff pad against an upper buff pad and rotating each pad at a different velocity.

Claim 21 (original) The method of claim 19, wherein the pad conditioning step is performed between each wafer being processed.

Claim 22 (canceled)

Claim 23 (original) The method of claim 8, wherein the material layer is comprised of aluminum, copper, or tungsten.

Claim 24 (original) The method of claim 8, wherein the barrier layer is comprised of Ti, TiN, Ta. or TaN.

Claim 25 (original) The method of claim 8, further comprising the step of:

- supplying a first polishing slurry to the primary polishing station; and
- d) supplying one or more different polishing slurries to the buff station.

Claim 26 (currently amended) The method of claim 8 wherein the step of removing the barrier layer comprises the step of removing the barrier layer using a Politer-Supreme buff pad having a volume compressibility of about 20.40 percent.

Claim 27 (previously presented) The method of claim 8 wherein the step of removing the barrier layer comprises the step of removing the barrier layer using a sturry comprising an abrasive suspension.

Claim 28 (previously presented) The method of claim 27 wherein the step of removing the barrier layer comprises the step of removing the barrier layer using a slurry comprising an aqueous acidic or basic solution.

Claim 26 (previously presented) The method of claim 8 further comprising the step of buffling the surface of the semiconductor water after the step of removing the burrier layer, the step of buffing the surface of the semiconductor water comprising buffling at the buff station using a buff paid and a second burrier layer stury comprising a diluted first burrier layer stury.